

5 address retrieval;

6 an internal column address generation module for burst read and write operation column

7 address generation; and

8 pipelined/burst circuitry coupled to the mode selection circuitry and configured to switch

9 between the pipelined mode and the burst mode for operating the storage device in either mode.

REMARKS

Claims 36-39 are amended, and claims 59-69 are added. As a result, claims 36-39 and 59-69 are now pending in this application.

CONCLUSION

Applicant believes the claims are in condition for allowance and requests reconsideration of the application and allowance of the claims. The Examiner is invited to telephone the below-signed attorney at (612) 373-6944 to discuss any questions which may arise with respect to the present application.

Respectfully submitted,

JEFFREY S. MAILLOUX ET AL.

By their Representatives,

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Date

16 Sept 1999

By

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